WHAT IS CLAIMED IS:

1. A flash memory with a NOR-gate architecture, comprising: 1 a) A data block allocated to storage of erase and write times for other data blocks in the 2 flash memory. 3 4 2. The memory of claim 1 wherein the processor is in communication with a host computer. 1 2 3. The memory of claim 2 wherein the host computer is a dedicated Internet device. ì 2 4. The memory of claim 1 wherein the table is also operable to provide a total number of 1 bytes for each storage operation. 2 3 5. The memory f claim 1 wherein the table is also operable to provide a number of erasures 1 2 for each block. 3 6. A method of managing NOR-gate architecture flash memory, the method comprising: 1 a) designating a table block of the flash memory operable to store erase and write times 2 for each block of the flash memory; 3 b) creating a most recent table by tracking time used by each block for erase and write 5 operations; 6 c) determining if the table block has enough space for the most recent table, wherein the 7 table block is erased if the table block does not have enough space; and 8 d) writing the new erase and write table into the table block.

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7. The method of claim 5, wherein the table is operable to store a number of erasures for 1 each block. 2 3 8. The method of claim 5, wherein the table block is determined to have enough space and 1 the new entry is written at the end of the most recent erase and write table. 2 3 1 9. The method of claim 5, wherein the table block is determined not to have enough space and the new entry is written at the beginning of the table block. 2 3 10. The method of claim 5 further comprising storing a number of erase and write operations 1 and size of storage operations in a table header. 2 3 11. A method of presenting progress of erase and write operations in a NOR-gate architecture 1 2 flash memory to a user, the method comprising: a) receiving a number of bytes to be stored in the flash memory; 3 b) determining the blocks to be used to store the number of bytes; 4 c) accessing a table containing erase and write times for each of the blocks to be used; 5 and 6 d) estimating the amount of time to store the number of bytes from the erase and write 7 times in the table. 8 9 12. The method of claim 10 further comprising tracking time used in erasing and writing to 1 2 the blocks to be used and updating the table with the time used.

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13. The method of claim 10, wherein the table accessed depends upon information contained in a table header. 14. The method of claim 10, wherein the estimated amount of time is updated and communicated in an iterative fashion. 15. The method of claim 10, wherein the estimated amount of time is only communicated once.